

AMENDMENTS TO THE CLAIMS

Claims 93 and 95-101 are currently pending in this application. Please amend Claim 93.

1.-92. (Canceled)

93. (Currently amended) A planar conductive material structure usable in manufacturing an interconnect for an integrated circuit comprising:

a substrate having a top portion that includes a surface portion and a cavity portion, wherein the cavity portion has at least a first cavity having a width of less than one micron and a second cavity having a width larger than 10 microns; and

a planar conductive layer, as deposited, that is formed within the cavity portion and on the surface portion such that a predetermined thickness range of the planar conductive layer over the surface portion is between one tenth and one half of the thickness of the planar conductive layer within the cavity portion.

94. (Canceled)

95. (Previously presented) The structure of claim 93, wherein the top portion includes an insulator layer and a barrier layer overlying said insulator layer and wherein the cavities are formed in the insulator layer.

96. (Previously presented) The structure of claim 93, wherein the planar conductive material comprises copper or copper alloy.

97. (Withdrawn) A conductive bead structure usable in manufacturing an interconnect for an integrated circuit comprising:

a substrate having a top portion that includes a surface portion and a cavity portion, wherein the cavity portion has at least a first cavity having a submicron width and a second cavity having a width larger than 10 microns; and

conductive beads formed within each cavity, wherein each conductive bead protrudes above a level of the surface portion of the substrate and is confined within a respective cavity.

98. (Withdrawn) The structure of Claim 97, wherein the top portion includes an insulator layer and a barrier layer overlying said insulator layer, and wherein the cavities are formed in the insulator layer.

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99. (Withdrawn) The structure of Claim 97, wherein the planar conductive material comprises copper or copper alloy.

100. (Previously presented) An integrated circuit including the structure of claim 93.

101. (Previously presented) The structure of claim 93, wherein the planar conductive material comprises copper.